



PATENTS  
ALT-155 CPA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Tony K. Ngai et al.  
Application No.: 09/124,649 Confirmation No.: 6941  
Filed : July 29, 1998  
(CPA filed May 10, 2001)  
For : PROGRAMMABLE LOGIC DEVICE HAVING  
EMBEDDED DUAL-PORT RANDOM ACCESS MEMORY  
CONFIGURABLE AS SINGLE-PORT MEMORY  
Group Art Unit : 2186  
Examiner : Pierre-Michel Bataille

New York, New York 10020  
July 30, 2002

Box ISSUE FEE  
Hon. Commissioner for Patents  
Washington, D.C. 20231

COMMENTS ON STATEMENT  
OF REASONS FOR ALLOWANCE

Sir:

The Examiner included a Statement of Reasons for Allowance in the Notice of Allowability dated June 5, 2002 in the above-identified patent application. As part of that statement, the Examiner stated (on page 3 of the Notice of Allowability) that "none of the prior art of record anticipates or renders obvious [certain] recited features" and then went on to set forth two limitations from applicants' claims as quotes. However, the "quotes" are not exact, and to the extent that the language set forth by the Examiner in the form of a quote differs from the actual claim limitations, applicants respectfully submit that their invention should be limited only by the claim language itself and not by the inexact quotes that appear in the Statement of Reasons for Allowance. Accordingly, applicants

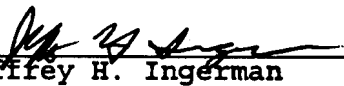
respectfully request that the Examiner reissue the Statement of Reasons for Allowance using exact quotes from the claims.

To assist the Examiner, applicants set forth in the table below their claim limitations as compared to the language "quoted" in the Statement of Reasons for Allowance:

Claim 1	Statement of Reasons for Allowance
a plurality of programmable interconnection resources for connecting conductors in said groups of interconnection conductors to one another and to said plurality of logic resources,	programmable interconnection resources for connecting conductors in a plurality of groups of interconnection conductors to one another and a plurality of logic resources,
a first programmable interconnection resource in said plurality of programmable interconnection resources for connecting port conductors in said read port to a selected one of said plurality of groups of interconnection conductors; and a second programmable interconnection resource in said plurality of programmable interconnection resources for connecting port conductors in said write port to said selected one of said plurality of groups of interconnection conductors;	where a first programmable interconnection resource connecting port conductors in a read port to a selected one of said plurality of groups of interconnection conductors and a second programmable interconnection resource connecting port conductors in a write port to a selected one of said plurality of groups of interconnection conductors;

Prompt issuance of a corrected Statement of  
Reasons for Allowance is respectfully requested.

Respectfully submitted,

  
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Kellian Davis  
\_\_\_\_\_  
Signature of Person Signing Certificate

7/30/02  
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